

07/18/00



SKJERVEN
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07-20-00

A

Docket No.: M-8203 US

July 18, 2000

Box Patent Application
Commissioner For Patents
Washington, D. C. 20231

Enclosed herewith for filing is a patent application, as follows:

Inventor(s): Yong E. Park; Jeongsik Yang; Shuen-Chin Chang; Young Gon Kim; Chiayao S. Tung;
Cindy Y. Ng

Title: System And Method For Multi-Symbol Interfacing

X Return Receipt Postcard
X This Transmittal Letter (in duplicate)
78 page(s) Specification (not including claims)
12 page(s) Claims
1 page Abstract
15 Sheet(s) of Drawings
4 page(s) Declaration For Patent Application and Power of Attorney
1 page(s) Recordation Form Cover Sheet (in duplicate)
5 page(s) Assignment
1 page(s) Statement of Inventorship

CLAIMS AS FILED

For	Number		Number		Rate		Basic Fee
	<u>Filed</u>		<u>Extra</u>				
Total Claims	50	-20 =	30	x	\$18.00 =	\$	\$690.00
						\$	540.00
Independent Claims	9	-3 =	6	x	\$78.00 =	\$	468.00
<input type="checkbox"/> Fee of _____ for the first filing of one or more multiple dependent claims per application							\$
<input type="checkbox"/> Fee for Request for Extension of Time							\$

Please make the following charges to Deposit Account 19-2386:

- ☒ Total fee for filing the patent application in the amount of \$ 1,698.00
☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account 19-2386.

EXPRESS MAIL LABEL NO:

EL030417625US

Respectfully submitted,

Philip W. Woo
Attorney for Applicants
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JCE77 U.S. PTO
09/617592
07/18/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Yong E. Park; Jeongsik Yang; Shuen-Chin Chang; Young Gon Kim;
Chiayao S. Tung; Cindy Y. Ng
Assignee: Integrated Memory Logic
Title: System And Method For Multi-Symbol Interfacing
Serial No.: Not Determined Filing Date:
Examiner: Not Determined Group Art Unit: Not Determined
Docket No.: M-8203 US

San Jose, California
July 18, 2000

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

STATEMENT OF INVENTORSHIP

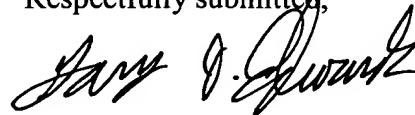
Dear Sir:

After a brief consultation with Dr. Ilbok Lee and Dr. Yong E. Park, it was determined that Dr. Lee is not an inventor of the above identified application. Accordingly, Dr. Lee did not sign the declaration or the accompanying assignment to Integrated Memory Logic, both of which are filed with the above identified application.

Express Mail Label No.

EL030417625US

Respectfully submitted,



Gary J. Edwards
Attorney for Applicant(s)
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